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Under the Page work Regueston Act of 1999, no parasits and res		
DISCLAIMER IN PATENT UNDER 37 CFR 1.321(a)		
lame of Palentee Ronday Technology Corporation	Docket Number (Optional)	
Patent Number 6,660,43	Data Patent Issued December 9, 2003	
Title of Invention METHOD OF MANUFACTURING INTEGRATED CIRCUIT DEVICE	an electronic device and a semiconductor	
I hereby disclaim the following complete claims in	n the above identified patent: Claims 1-26	
The extent of my interest in earld patent is (if assume the exignment is recorded): Assignee of entity	ignae of record, state liber and page, or real and frame, n infarcat by Assignment recorded at Real 014244. Frame 0278.	
The fee for this disclaimer is set forth in 37 CFR	1.20(d).	
Patentee claims small entity status. Sea 37 CFR 1.27.		
Small entity status has already been established in this case, and is still proper.		
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	31,568	
Signature	Registration Number, if applicable	
Mitchell W. Shapi		
. Typed or printed name of patentee/att	-	
Milles & Stockbridge P.C., 1751 Pinn	usele Drive, Suite 590	
Address ptcLean, VA 221	- na	
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This collection of information is required by 37 CFR 1.221. The information is required to obtain or retain a bonedit by the public which is to the (and by the USPTO to process) and application. Confidentiably is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is extracted to take 18 minutes to complete, including galluting, preparing, and quantities the completed application from the USPTO. Terms was very departing upon the techniques can be application from the USPTO. Terms was very departing upon the techniques can be applied that the term inducing the burden, should be about to the chief information office. U.S. Opportment of Commission P.O. Box 1460, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS; SEND TO Commissioner for Primers, P.O. Box 1460, Alexandria, VA 22313-1460.

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PAGE 8/10 * RCVD AT 1/27/2006 5:07:19 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/33 * DNIS:2738300 * CSID:+7036841157 * DURATION (mm-ss):02-26

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XA-9566 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the patent of:

Toshihiko TANAKA et al.

Patent No. 6,660,438

Appln. No.: 09/983,172

Filed: October 23, 2081

Issued: December 9, 2003

For: METHOD OF MANUFACTURING AN SLECTRONIC DEVICE AND A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

DISCLAIMER OF ENTIRE TERM OF PATENT

commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

STE:

Renesas Technology Corporation (hereinafter, "Patent Owner") is owner of the entire interest in the instant patent by virtue of an Assignment recorded at reel 014244. frame 0278.

Patent Owner hereby disclaims, and dedicates to the public, the entire term of the instant patent.

The undersigned is an attorney of record.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the

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filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted.

MWS:SJK

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January 11, 2006